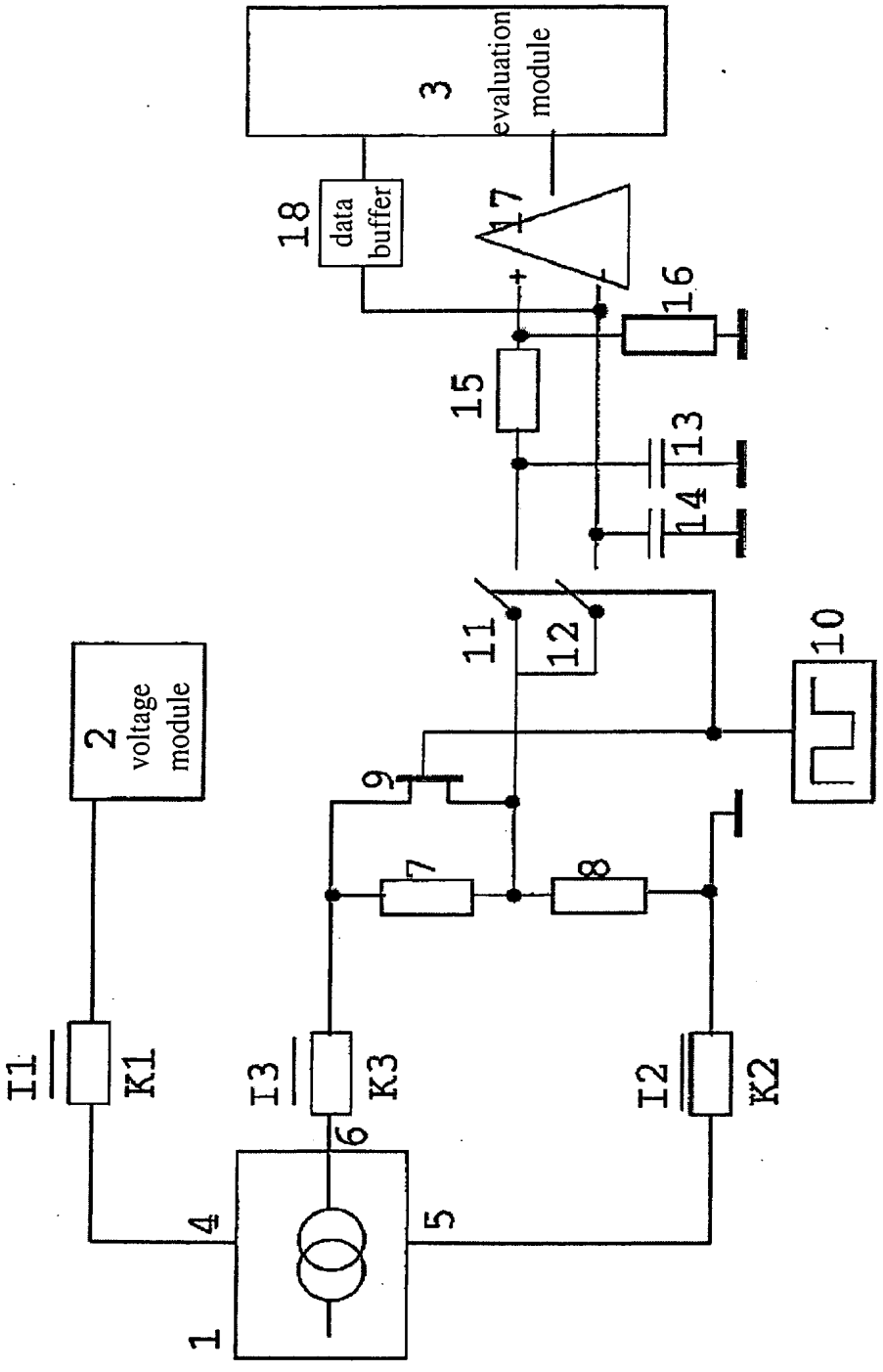
3
evaluation module
18
data buffer
17
16
15
13
14
11
12
10
2
voltage module
9
7
8
I1
K1
I3
K3
I2
K2
6
4
5
1

Fig. 1